

VENKATESAN D

M.Tech Electronics & Communication Engineering

☎ +91-8526974535

✉ dvenkat101@gmail.com



ACADEMIC PROFILE

M.Tech	CGPA: 8.25	Indian Institute of Science (IISc)	2026
B.E	CGPA: 8.17	Erode Sengunthar Engineering College	2019
Class XII (TN Board)	92.5%	The Sengunthar Higher Secondary School	2015
Class X (TN Board)	96.6%	Sengunthar Higher Secondary School	2013

PROJECTS

Evaluation of Dual Stack and Dual Connectivity using MPTCP

- Sole contributor for the project in collaboration with **Qualcomm**, responsible for end-to-end system design, implementation, and analysis.
- Study and implementation of Dual Stack and Dual Connectivity architectures using multi-path transport protocols such as MPTCP.
- System-level performance evaluation focusing on throughput, latency, reliability, and failover behaviour.
- Currently preparing a **research paper** based on the outcomes of this project.
- Ability to diagnose system-level issues across protocol stack boundaries.

Li-Fi Audio Communication System

- Designed and implemented a Li-Fi based audio transmission system using visible light communication.
- Audio signals transmitted via LED intensity modulation and recovered using a photodiode receiver.
- Implemented amplification, filtering, and signal conditioning for reliable audio reconstruction.
- Demonstrated feasibility of short-range optical wireless communication.

COURSES

- Stochastic Processes and Queueing Theory (PG)
- Mean Field Asymptotics and Applications (PG)
- Wireless Communication (PG)
- Information Theory (PG)
- Detection and Estimation Theory (PG)
- Communication Networks (PG)
- Introduction to Integrated Circuit Design (PG)
- Digital Communication (PG)
- Random Processes (PG)
- Matrix Theory (PG)

TEACHING ASSISTANTSHIP

- Teaching Assistant for **Communication Networks** (Aug – Dec 2025).
- Teaching Assistant for **Random Processes (Online)** (Aug – Dec 2025).
- Teaching Assistant for **Stochastic Processes and Queueing Theory** (Jan – Apr 2026).

FELLOWSHIP

- Awarded the **CISCO CNI Fellowship** for the academic year 2025–2026.

ACHIEVEMENTS

- Got a GATE AIR Rank of 534 with the score of 712 in GATE 2023 ECE.
- Topper of the ECE department at Erode Sengunthar Engineering College.
- Winner of the **EXCELL AWARD** at Tamil Nadu state-level general knowledge quiz competition (JAYCEES, Nov 2010).

EXPERIENCE

- Worked as an intern in Vernalis Systems from Aug 2018 to Mar 2019.
- Worked as a Junior Software developer at TCS from July 2019 to Oct 2021.
- Worked as a Product developer at Vernalis Systems from Nov 2021 to Feb 2023.

SKILLS

- Programming: Python, C; modeling and simulation using MATLAB.
- Website development experience; previously maintained “VEDify’s” (now offline).
- Developing a Finance (Digital Wallet) app.
- Digital design using Inkscape and basic video editing.

INTERESTS

- Reading about economics and cutting-edge technology.
- Travelling and exploring new places.
- Playing violin and listening to music.

LANGUAGES

English, Tamil